

PATENT NUMBER

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| AK | O.I.P.E. | PATENT DATE |
| SCANNED 43 | QA 1A | |

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| APPLICATION NO. | CONT/PRIOR | CLASS | SUBCLASS | ART UNIT | EXAMINER |
| 09/941447 | D | 438 | 306 | 222 2812 | Thomas |

APPLICANTS

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Oleg Siniaguine

Title

Semiconductor structures having multiple conductive layers in an opening, and methods for fabricating same

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| <input type="checkbox"/> TERMINAL DISCLAIMER | DRAWINGS | | | CLAIMS ALLOWED | |
| | Sheets Drwg. | Figs. Drwg. | Print Fig. | Total Claims | Print Claim for O.G. |
| <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. | _____ (Assistant Examiner) _____ (Date) | | | NOTICE OF ALLOWANCE MAILED | |
| <input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ | _____ (Primary Examiner) _____ (Date) | | | ISSUE FEE | |
| _____ | _____ | | | Amount Due | Date Paid |
| <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed. | _____ (Legal Instruments Examiner) _____ (Date) | | | ISSUE BATCH NUMBER | |

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